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## APPLICANTS

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## \*\* CONTINUING DATA \*\*\*\*\*

This application is a DIV of 09/812,947 03/27/2001 PAT 6,725,440

*lh*

## \*\* FOREIGN APPLICATIONS \*\*\*\*\*

JAPAN 2000-085775 03/27/2000

*lh*

## IF REQUIRED, FOREIGN FILING LICENSE GRANTED

\*\* 04/09/2004

Foreign Priority claimed	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR	SHEETS	TOTAL	INDEPENDENT
35 USC 119 (a-d) conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	COUNTRY	DRAWING	CLAIMS	CLAIMS
Verified and Acknowledged	<i>lh</i> Examiner's Signature	JAPAN	6	6	1

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## TITLE

Semiconductor integrated circuit device and exposure method

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